AMENDMENTS TO THE SPECIFICATION:

Please amend the paragraphs on page 10, lines 1-5, as follows:

~~Figs. 3 and 4 are schematic diagrams illustrating the mechanism of context association in the associative memory-based computer of the present invention.~~

Fig. 3 is a schematic diagram illustrating the mechanism of context association in an upper abstraction level of the associative memory-based computer of the present invention.

Fig. 4 is a schematic diagram illustrating the mechanism of context association in a lower abstraction level of the associative memory-based computer of the present invention.

~~Figs. 5-8 are first through fourth schematic diagrams illustrating a basic sequence of the present invention.~~

Fig. 5 is a schematic diagram illustrating a first phase in a basic sequence of the present invention.

Fig. 6 is a schematic diagram illustrating a second phase in a basic sequence of the present invention.

Fig. 7 is a schematic diagram illustrating a third phase in a basic sequence of the present invention.

Fig. 8 is a schematic diagram illustrating a fourth phase in a basic sequence of the present invention.